

Notice of Allowability	Application No.	Applicant(s)	
	10/690,606	HAYASHI ET AL.	
	Examiner	Art Unit	
	Quochien B. Vuong	2685	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to 10/23/2003.

2. The allowed claim(s) is/are 1-11.

3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some* c) None of the:

1. Certified copies of the priority documents have been received.

2. Certified copies of the priority documents have been received in Application No. _____.

3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: JAPAN 2002-338250.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.

5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.

(a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached

1) hereto or 2) to Paper No./Mail Date _____.

(b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- 1. Notice of References Cited (PTO-892)
- 2. Notice of Draftperson's Patent Drawing Review (PTO-948)
- 3. Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____
- 4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
- 5. Notice of Informal Patent Application (PTO-152)
- 6. Interview Summary (PTO-413),
Paper No./Mail Date _____
- 7. Examiner's Amendment/Comment
- 8. Examiner's Statement of Reasons for Allowance
- 9. Other _____

Reasons for Allowance

1. Claims 1-11 are allowed over the cited prior art.
2. The following is an examiner's statement of reasons for allowance:

Regarding independent claim 1, Doetsch et al. (US 6,847,812) disclose a semiconductor integrated circuit device (see figure 1) comprising: a demodulation circuit (3 and 4) for demodulating a reception signal; a first oscillation circuit for generating a signal to be combined with the reception signal or an oscillation signal as a base of the signal; a modulation circuit (5) for modulating a transmission demodulating a signal; a second oscillation circuit generating a signal to be combined with the transmission signal or an oscillation signal as a base of the signal; a reference oscillation circuit (8) for generating reference frequency signal which determines the frequency of each oscillation signals generated by the first and second oscillation circuits; first (800) and second (9) external terminals provided in correspondence with the reference oscillation circuit (column 3, line 28 – column 6, line 6); wherein the reference oscillation circuit (figure 3) includes a transistor (T), capacitor (C), and resistive element (R) (column 6, lines 28-40; and figure 3). However, Doestch et al. do not teach or fairly suggest the semiconductor integrated circuit device wherein the reference oscillation circuit includes a transistor of which control terminal is connected to the first external terminal, capacitor element connected between the first and second external terminals, and a resistive element connected between the second external terminal and power source voltage terminal, the reference oscillation circuit can oscillate when a part of an output of the transistor is fed back to the control terminal of the transistor, and the second external

terminal is set to a predetermined potential or an open state depending on either an oscillator or an electronic part for oscillation for outputting an oscillation signal connected the first external terminal.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Priority

3. Acknowledgment is made of applicant's claim for foreign priority based on an application filed in Japan on 11/21/2002. It is noted, however, that applicant has not filed a certified copy of the JAPAN 2002-338250 application as required by 35 U.S.C. 119(b).

Conclusion

4. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Weise (US 4,068,173) discloses frequency stabilized microwave signal source.

Fenk (US 4,479,259) disclose transistor oscillator circuit.

Mohindra (US 6,442,380) discloses automatic gain control in a zero intermediate frequency radio device.

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5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Quochien B. Vuong whose telephone number is (571) 272-7902. The examiner can normally be reached on M-F 9:30-18:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Edward Urban can be reached on (571) 272-7899. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Quochien B. Vuong
Feb. 04, 2006.



QUOCHIEN B. VUONG
PRIMARY EXAMINER